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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Mansoori et al. Art Unit: 2812  
Serial No.: 10/620,492 Examiner: Lindsay, Jr., W.  
Filing Date: 07/16/2003 Docket No.: TI-35375  
Customer No.: 23494 Conf. No.: 9069  
Title: METHOD TO REDUCE TRANSISTOR GATE TO SOURCE/DRAIN OVERLAP  
CAPACITANCE BY INCORPORATION OF CARBON

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<b>NAME OF INVENTOR(S):</b> <b>Mansoori</b>	
<b>TITLE OF INVENTION:</b> <b>Method to Reduce Transistor . . .</b>	
<b>TI FILE NO.:</b> <b>TI-35375</b>	<b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>
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